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This book introduces a modern approach to embedded system design, presenting software design and hardware design in a unified manner. It covers trends and challenges, introduces the design and use of single-purpose processors ("hardware") and general-purpose processors

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(**Micro**software"), describes memories and buses, illustrates hardware/software tradeoffs using a digital camera example, and discusses advanced computation models, controls systems, chip technologies, and modern design tools. For courses found in EE, CS and other engineering departments.

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Optimizations and Tradeoffs7. Physical
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This volume presents the technical program of the 2007 International Embedded Systems Symposium held in Irvine, California. It covers timely topics, techniques and trends in embedded system design, including design methodology, networks-on-chip, distributed and networked systems, and system verification. It places emphasis on automotive and medical applications and includes case studies and special aspects in embedded system design.

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Synthesis and Verification introduces a model-based approach to system level design. It presents modeling techniques for both computation and communication at different levels of abstraction, such as specification, transaction level and cycle-accurate level. It discusses synthesis methods for system level architectures, embedded software and hardware components. Using these methods, designers can develop applications with high level models, which are automatically translatable to low level implementations. This book, furthermore, describes simulation-based and formal verification methods that are essential for achieving design confidence. The book concludes with an overview of existing tools along with a design case study outlining the practice of embedded system design. Specifically, this book addresses the following topics in detail: . System

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Modeling at different abstraction levels .
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Hardware/Software codesign . Software
and Hardware component synthesis .
System verification This book is for groups
within the embedded system community:
students in courses on embedded systems,
embedded application developers, system
designers and managers, CAD tool
developers, design automation, and system
engineering.

This book offers readers a set of new
approaches and tools a set of tools and
techniques for facing challenges in
parallelization with design of embedded
systems. It provides an advanced parallel
simulation infrastructure for efficient and
effective system-level model validation and
development so as to build better products

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In less time. Since parallel discrete event simulation (PDES) has the potential to exploit the underlying parallel computational capability in today ' s multi-core simulation hosts, the author begins by reviewing the parallelization of discrete event simulation, identifying problems and solutions. She then describes out-of-order parallel discrete event simulation (OoO PDES), a novel approach for efficient validation of system-level designs by aggressively exploiting the parallel capabilities of todays ' multi-core PCs. This approach enables readers to design simulators that can fully exploit the parallel processing capability of the multi-core system to achieve fast speed simulation, without loss of simulation and timing accuracy. Based on this parallel simulation infrastructure, the author further describes automatic approaches that help the designer quickly to narrow

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down the debugging targets in faulty ESL models with parallelism.

In this new edition the latest ARM processors and other hardware developments are fully covered along with new sections on Embedded Linux and the new freeware operating system eCOS. The hot topic of embedded systems and the internet is also introduced. In addition a fascinating new case study explores how embedded systems can be developed and experimented with using nothing more than a standard PC. * A practical introduction to the hottest topic in modern electronics design * Covers hardware, interfacing and programming in one book * New material on Embedded Linux for embedded internet systems

A presentation of developments in microcontroller technology, providing

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lucid instructions on its many and varied applications. It focuses on the popular eight-bit microcontroller, the 8051, and the 83C552. The text outlines a systematic methodology for small-scale, control-dominated embedded systems, and is accompanied by a disk of all the example problems included in the book.

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